

## REMARKS

Applicants' Attorney has requested various changes in the drawings to clarify their content and to correct various errors. In the following explanation of these changes, all page numbers refer to pages in the specification.

For Fig. 1, Applicants' Attorney has specifically requested that:

(a) The label "40 Mb/s" for the arrow extending between blocks 52 and 22 be changed to "DVB Data" in conformity with the disclosure on pages 13 and 20 that network port 22 receives DVB data from network interface 52;

(b) The label "11 Mb/s" for the arrow extending between blocks 46 and 24 be changed to "DVD Data" in conformity with the disclosure on pages 6 and 13 that DVD-DSP interface 24 receives DVD data from DVB-DSP 46;

(c) The label "DVB Byte Stream" be added to the arrow extending between blocks 22 and 26 in conformity with the disclosure on pages 13 and 20 that network port 22 reformats the incoming DVB data into bytes and furnishes the reformatted DVB data to stream demultiplexer 26;

(d) The label "DVD Byte Stream" be added to the arrow extending between blocks 24 and 26 in conformity with the disclosure on pages 6 and 13 that DVD-DSP interface 24 reformats the incoming DVD data into bytes and furnishes the reformatted DVD data to stream demultiplexer 26;

(e) The label "SI Nav Vid Aud Sub\_Pict VBI" for the arrow extending between blocks 26 and 48 be changed to "Compressed Video and Audio Data" in conformity with the disclosure at pages 6, 14, and 21 that stream demultiplexer 26 provides the compressed video and audio data to the respective video and audio sub-buffers of buffer 48;

(f) The label "Aud" for the arrow extending between blocks 48 and 34 be changed to "Compressed Audio Data" in conformity with the disclosure on pages 13 and 15 that audio decoder 34 decodes the incoming compressed audio data;

(g) The label "Vid" for the arrow extending between blocks 48 and 36 be changed to "Compressed Video Data" in conformity with the disclosure at page 6 that video decoder 36 decodes the incoming compressed video data;

(h) The label "Decoded Audio Data" be added to the arrow extending between blocks 50 and 38 in conformity with the disclosure at pages 13 and 14 that audio output processor 38 receives the decoded audio data from buffer 50;

(i) The label "Vid Sub" for the arrow extending between blocks 50 and 40 be changed to "Decoded Video Data" in conformity with the disclosure on pages 6 and 12 that video output processor 40 receives the decoded video data from buffer 50;

(j) The signal names "Dec\_Time\_Stamp; Buffer\_Ptr; ream\_ID or ble\_IC, Section\_ID", "Aud\_Dec\_Time Aud\_Sync\_Ptr", and "Sys\_Time" be respectively changed to their longer forms for improving clarity;

(k) One arrow labeled "Tags" extend from block 26 to block 48, and another arrow labeled "Tags" extend from block 48 to block 54, in conformity with the disclosure on pages 9 - 12 and 25 - 27 that tags generated by stream demultiplexer 26 are stored in a sub-buffer of buffer 48 for access by CPU 54;

(l) The label "TDPs" be added to the arrow extending from block 54 to block 36 in conformity with the disclosure at page 10 that CPU 54 provides task definition packets ("TDPs") to video decoder 36;

(m) The notation "Vsync Phase" (both occurrences) be changed to "VsyncPhase" to clearly distinguish this notation from reference symbol "Vsync" for the synchronization signal;

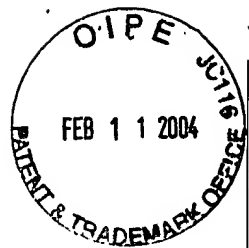
(n) The line extending between blocks 30 and 26 be converted into an upward-pointing arrow to indicate that timer 30 provides the system time to stream demultiplexer 26;

(o) A dot be placed at each location where an arrow divides into branches;  
and

(p) The routing for the Vsync and TDPs arrows be shifted slightly to make the figure easier to understand.

For the remaining drawings, Applicants' Attorney has requested that

(aa) The labels "Transport Engine FIFO" and "Audio Sync Tag FIFO" in Fig. 5 be respectively changed to "Transport Engine Buffer" and "Audio Sync Tag Buffer"



inasmuch as the sub-buffers, including audio sub-buffer 100, of buffer 48 although acting as FIFOs in software are typically not implemented as FIFOs in hardware;

(bb) Reference symbols "50", "52", and "54" for the MMU, host bus interface, and network port/DVD controller be respectively changed to "60", "62", and "64" since reference symbols "50", "52", and "54" are respectively utilized earlier for other items in Fig. 1;

(cc) Reference symbol "100" for the byte stream in Fig. 8 be changed to "80" since reference symbol "100" is utilized earlier for the audio buffer in Fig. 5; and

(dd) Reference symbols "106" and "108" for the message queue and tag in Fig. 9 be respectively changed to "120" and "122" since reference symbol "106" is utilized earlier for another item in Fig. 5.

Further comments relative to the requested changes in Figs. 5, 6, 8, and 9 are presented in the accompanying amendment to text.

Please telephone Attorney for Applicant(s) at 650-964-9767 if there are any questions.

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Respectfully submitted,

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